Certificate / Certificat
Zertifikat / 合格証

FLO 2209117 C001

exida hereby confirms that the:

D20 Positioner

PMV Automation AB
Solna, Sweden

Has been assessed per the relevant requirements of:

IEC 61508 : 2010 Parts 1-2

and meets requirements providing a level of integrity to:

Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type A, Route 2_H Device

PFH/PFD_{avg} and Architecture Constraints must be verified for each application

Certified Models:
Only the Model numbers listed in Section 3 of the Assessment Report are included.

Safety Function:
The Positioner will move to the designed fail-safe state upon the removal of analog input power within the specified safety time.

Application Restrictions:
The unit must be properly designed into a Safety Instrumented Function per the Safety Manual requirements.

The manufacturer may use the mark:

Revision 1.1 May 30, 2025
Surveillance Audit Due
November 1, 2028

ANSI National Accreditation Board
ACCREDITED
ISO/IEC 17065
PRODUCT CERTIFICATION BODY
#1004

Evaluating Assessor

Certifying Assessor

Certificate / Certificat / Zertifikat / 合格証

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Systematic Capability: SC 3 (SIL 3 Capable)

Random Capability: Type A, Route 2_H Device

PFH/PFD_{avg} and Architecture Constraints must be verified for each application

D20 Positioner

Systematic Capability :

The product has met manufacturer design process requirements of Safety Integrity Level (SIL) 3. These are intended to achieve sufficient integrity against systematic errors of design by the manufacturer.

A Safety Instrumented Function (SIF) designed with this product must not be used at a SIL level higher than stated.

Random Capability:

The SIL limit imposed by the Architectural Constraints must be met for each element. This device meets *exida* criteria for Route 2_H.

IEC 61508 Failure Rates in FIT[1]

Application/Device/Configuration	λ_{SD}	λ_{SU}	λ_{DD}	λ_{DU}
Single Acting	0	185	0	181
Single Acting, with PVST	183	2	115	66

[1] FIT = 1 failure / 10^9 hours

[2] PVST = Partial Valve Stroke Test of a final element Device

SIL Verification:

The Safety Integrity Level (SIL) of an entire Safety Instrumented Function (SIF) must be verified via a calculation of PFH/PFD_{avg} considering redundant architectures, proof test interval, proof test effectiveness, any automatic diagnostics, average repair time and the specific failure rates of all products included in the SIF. Each element must be checked to assure compliance with minimum hardware fault tolerance (HFT) requirements.

The following documents are a mandatory part of certification:

Assessment Report: FLO 18-10-095 R002 V1R2 PMV D30, D20 Assessment Report (or later)

Safety Manual: D30 safety manual v1 r2 (or later)

80 N Main St
Sellersville, PA 18960